Process Change Notification Form

PCN Number:	PCN_0229_CS5466_revD1
Date of Notification:	06/12/08
Cirrus Logic P/N(s):	CS5466-IS, CS5466-ISR, CS5466-ISZ, CS5466-ISZR, CS5460F-ISZ, CS5460F-ISZR
Date PCN Effective:	06/12/08
Reason for Change:	☒ Design /New Rev. ☐ Fab Site ☐ Fab Process ☐ Additional Fab Source ☐ Assembly Site ☐ Assembly Process ☐ Additional Assembly Source ☐ Other (specify)
Description of Change:	☐ Fix errata ☐ Yield enhancement ☐ Fix known bug ☒ Performance Improvement ☐ Other Performance improvement of higher dynamic range in the pulse outputs, no-load threshold capability of 0.004% of full-scale. These devices are lead-free, RoHS compliant.
Cirrus Logic P/N Change:	☐ Yes, New Part Number: ☒ No
Pack Mark Change:	☒ Yes ☐ No ***If Yes, briefly explain:*** From: YFNA**CX**HAYYWW To: YFNA**D1**HAYYWW *[Any Fab, Assembly, or Design changes results in pack mark changes, please provide detail]*
Lot Effective Date:	07/20/08 *[Contact the area sales representative for availability of samples if applicable]*
Quality & Reliability impact:	Qualification Data: ☒ Required ☐ Not Required ****See pages below**
Datasheet Change Required?	☒ Yes ☐ No If Yes, briefly explain: See Technical Bulletin Attached ****See pages below**
Software Change Required?	☒ Yes ☐ No If Yes, briefly explain: Customers not interested in the new functionality can switch to this new revision with no hardware or software changes.

RELIABILITY QUALIFICATION REPORT

Job #: QJ1633

Subject/Purpose:

This is to qualify the CS5466 and CS5460F rev D1 devices.

APPROVALS:

Michael Klucher

Reliability Engineering

Results:

Qualification complete.

STATUS:

Pass

Stress	Conditions	Method	Duration	Lot	Results (Fail/Sample)
ESD Machine Model	25 °C	JESD22-A115	200 Volts	1	0/5
ESD Human Body Model	25 °C	JESD22-A114	2000 Volts	1	0/5
Latch-Up VDD	85 °C 1.5*Vmax Volts	JESD78	1.5*Vmax Volts	1	0/6
				1	0/6
			1.5*Vmax Volts	1	0/6

Background Information:

Part #: CS5466 **Rev:** D1 **Fab:** MagnaChip (F) **Lead Finish:** Pb-free

Package: 24SSOP **Assembly:** Amkor P1 (Philippines)

Prepared by: Mike Klucher

CONFIDENTIAL

CS5466

Technical Bulletin: Functional & Performance Improvements in CS5466 rev D Silicon

(Reference CS5466 data sheet revision DS659F1 dated AUG '05).

Dynamic Range of Pulse Outputs (/E1, /E2, FOUT)

The dynamic range of the pulse outputs has been improved in rev D. When using the pulse outputs to measure energy accuracy, the device now meets all specified typical accuracy levels listed in the Characteristics and Specifications section of the data sheet.

No Load Threshold

Revision C:

Revision C of the CS5466 has a set no load threshold.

Revision D:

Revision D of the CS5466 has been modified to include a no load threshold of 0.004% of full-scale. When the magnitude of the power calculated is less than 0.004% of full scale, the active energy pulse output will be disabled.

Anti-creep / Start-up Pulses

Revision C:

Revision C of the CS5466 calculated energy during startup as the high-pass filters settled.

Revision D:

Revision D of the CS5466 has been modified to suppress energy pulses until the high-pass filters have settled.

Cirrus Logic PCN administrator: Cirrus Logic - Customer Quality

Digitally signed by Cirrus Logic - Customer Quality
DN: CN = Cirrus Logic - Customer Quality, C = US, OU = Cirrus Logic Austin
Reason: Approved - signatures on file
Date: 2008.06.18 09:10:42 -05'00'

Acknowledgement of Receipt of Notice:

Does customer waive PCN Effective Date? ☐ **YES** ☐ **NO**

Company Name: _____

Name (please print)**:** _____ **Title:** _____

Signature: _____ **Date:** _____

Customer Representative is to obtain the customer acknowledgement/signature and return this notification to Cirrus Logic Corp. Quality, attn: PCN administrator at fax number (512) 851-4656

NOTE: Lack of acknowledgement within 30 days of the date of notice, constitutes acceptance of change.
(Reference JEDEC Industry Standard: JESD-46)