

Customer Process Change Notification Form PCN-2011-424

Part Information

Cirrus Logic Part # / Quantity

CS5346-CQZR, CS5346-CQZR, CS5346-DQZ, CS5346-DQZR

PCN Effective Date:

Lot Effective Date:

Cirrus P/N Change: Yes No

(Contact the sales representative for availability of samples if applicable)

If yes, provide new part number:

Package Mark Change: Yes No

If Yes, briefly explain:

[Any Fab, Assembly, or Design changes results in pack mark changes, please provide detail]

Reason for Change:

- Design/New Rev
- Assembly Site
- Fab Site
- Other (Specify)
- Fab Process
- Additional Assembly Source
- Assembly Process
- Additional Fab Source

Description of Change:

- Fix Errata
- Fix Known Bug
- Other (specify)
- Yield Enhancement
- Performance Improvement

Reduce flag paddle size for the above listed devices assembled in 48 LD LQFP.
Summary of Bill of Material Change:

48 LQFP	SPIL Current BOM	SPIL New Standard BOM
Mold Compound	Sumitomo G700	No Change
Die Attach	Sumitomo 1033BF	No Change
Wire	0.8 mil Au	No Change
Leadframe	0.160" x 0.160"	0.120" x 0.120"
Finish	Matte Sn, 100%	No Change

Samples available upon request.



Qualification Reliability Report

Report: QRR110102
Date: 2/2/2011
Approved by: Mike Klucher

Purpose	Results
Qualification of Lead Frame dimension change on the CS5346 product family.	Qualification successful.

Background Information					
Part #:	CS5346	Fab:	MagnaChip F4 (Korea)	Package:	48 LQFP
Rev:	A1	Assembly:	Siliconware (Taiwan)	Lead Finish:	Pb-free Matte tin plate

Stress Name	Method	Conditions	Lot	Read Point	Results (Fail/Sample)
Precondition MSL-3	JESD22-A113	24HR 125 °C Bake 192HR 30°C/60%RH Soak 3 pass 260 °C reflow	1	Precondition	0 / 231
			2	Precondition	0 / 231
			3	Precondition	0 / 231
HAST	JESD22-A110	130 °C 85 %RH With Bias Static	1	96 Hours	0 / 77
			2	96 Hours	0 / 77
			3	96 Hours	0 / 77
Temperature Cycle	JESD22-A104	Condition C -65°C +150°C air to air	1	500 Cycles	0 / 77
			2	500 Cycles	0 / 77
			3	500 Cycles	0 / 77
Tomography (CSAM)	J-STD-035		1	Post Temp Cycle	0 / 11
			2	Post Temp Cycle	0 / 11
			3	Post Temp Cycle	0 / 11
Autoclave/PPOT	JESD22-A102	121 °C 100% R.H.	1	96 Hours	0 / 77
			2	96 Hours	0 / 77
			3	96 Hours	0 / 77
Solderability	JESD22-B102	93 °C steam aging 8 Hours 245 °C solder bath 5 Seconds	1	Solderability	0 / 15
HTSL (High Temperature Storage Life)	JESD22-A103	150 °C	1	1000 Hours	0 / 77

Quality and Reliability Impact:Qualification Data Required? Yes No

Data Sheet Change Required? Yes No

If Yes, briefly explain:

Software Change Required? Yes No

If Yes, briefly explain:


Customer Acknowledgement and Agreement**Acknowledged and Agreed as of the Date written below:**Customer Company _____
Name:Customer Contact _____
Name:

Title: _____

Signature: _____

Date: Customer Agreed Customer Reject Not Applicable

Customer Comments:

	Title: CUSTOMER PROCESS CHANGE NOTIFICATION FORM		
	Digitally Signed By:	Doc No: 4-QUAL-00017	Rev: D

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